

0897 D 7880

THIRD ANGLE PROJECTION

A - GA  
SET B REGISTER  
A - GB  
ADD

A - GC  
NEGATE AND ADD

A - GB  
STORE Q REGISTER  
A - GB  
READ

TO SHEET 2

MATRIX ADDRESS	S2 S4 CA C	OPERATION	WAVE-FORMS	TIMING
C6A	S2 S4 CA C	Transfer function into I.reg. Set J.reg with address of B.reg. and then read contents of B.reg. into M.reg.	WRITE READ GTQ T4 DTQ T3 OTJ T3 DTM T3 MTI T2 VTG T2 OTI T1 OTG T1 ETJ K1 DTF1 MTF	*

MATRIX ADDRESS	S3 S4 C	OPERATION	WAVE-FORMS	TIMING
C7	S3 S4 C	Add contents of M.reg. and Q.reg. and put result in M.reg. Enable Function Decode.	GTM T4 DTM T3 OTJ T3 FTG T2 OTG T1 SFD MT DTF	*

MATRIX ADDRESS	S2 S4 CA C	OPERATION	WAVE-FORMS	TIMING
C6B	S2 S4 CA C	Transfer function from M.reg. into I.reg. and enable Function Decode	GTM T4 DTM T3 OTJ T3 MTI T2 VTG T2 OTI T1 OTG T1 SFD DTF1 MTF	*

MATRIX ADDRESS	S0 S4 DD DH	OPERATION	WAVE-FORMS	TIMING
00	S0 S4 DD DH	Transfer address from M.reg. into J.reg. then read number from that address into M.reg.	MTF DTF1 DTF2 OTG T1 VTG T2 OTM T3 GTJ T4 READ WRITE	*

MATRIX ADDRESS	S0 S4 DB DH	OPERATION	WAVE-FORMS	TIMING
10	S0 S4 DB DH	Transfer address from M.reg. into J.reg. then read number from that address into M.reg.	MTF DTF1 DTF2 OTG T1 VTG T2 OTM T3 GTJ T4 READ WRITE	*

MATRIX ADDRESS	S0 S4 DC DH	OPERATION	WAVE-FORMS	TIMING
20	S0 S4 DC DH	Transfer address from M.reg. into J.reg. then read number from that address into M.reg.	MTF DTF1 DTF2 OTG T1 VTG T2 OTM T3 GTJ T4 READ WRITE	*

MATRIX ADDRESS	S0 S4 DA DH	OPERATION	WAVE-FORMS	TIMING
30	S0 S4 DA DH	Transfer address from M.reg. into J.reg. and set overflow unit to zero.	MTF DTF1 DTF2 OTG T1 VTG T2 OTX T3 GTJ T4	*

MATRIX ADDRESS	S0 S4 DD DF	OPERATION	WAVE-FORMS	TIMING
40	S0 S4 DD DF	Transfer address from M.reg. into J.reg. then read number from that address into M.reg.	MTF DTF1 DTF2 OTG T1 VTG T2 OTM T3 GTJ T4 READ WRITE	*

MATRIX ADDRESS	S1 S4 C	OPERATION	WAVE-FORMS	TIMING
C5	S1 S4 C	Test for B modifier and set J.reg to zero.	TM18	*

MATRIX ADDRESS	S0 S4 C	OPERATION	WAVE-FORMS	TIMING
C4	S0 S4 C	Increase M.reg. by 1 then transfer into J.reg. Read contents of that address into M.reg.	WRITE READ GTJ T4 DTM T3 OTJ T3 FTG T2 OTG T1 DTF2 DTF1 MTF	*

MATRIX ADDRESS	S3 S4 C	OPERATION	WAVE-FORMS	TIMING
C3	S3 S4 C	Decrease M.reg. by 1 then transfer into J.reg. Read contents of that address into M.reg.	WRITE READ GTJ T4 DTM T3 OTJ T3 FTG T2 OTG T1 DTF2 DTF1 MTF	*

MATRIX ADDRESS	S2 S4 C	OPERATION	WAVE-FORMS	TIMING
C2	S2 S4 C	Increase M.reg. by 1 and rewrite in SCR.	WRITE GTM T4 DTM T3 FTG T2 OTG T1 ITF MTF	*

MATRIX ADDRESS	S1 S4 CA C	OPERATION	WAVE-FORMS	TIMING
C1A	S1 S4 CA C	Transfer M.reg. into A.reg.	GTA T4 OTA T3 FTG T2 OTG T1 MTF	*

MATRIX ADDRESS	S1 S4 CA C	OPERATION	WAVE-FORMS	TIMING
C1B	S1 S4 CA C	Transfer M.reg. into A.reg.	GTA T4 OTA T3 FTG T2 OTG T1 MTF	*

MATRIX ADDRESS	S0 S4 C	OPERATION	WAVE-FORMS	TIMING
C0	S0 S4 C	Transfer word gen. into M. reg. & test NGA. Clear J.reg. & ignore extnl. interrupts.	WTM T4 DTM T3 OTJ T3 TNGA DTE	*

ENTRY POINT WHEN USING ENTER, OBEY OR JUMP KEYS.

\* Waveforms set true at T6 of previous timing cycle and set false at T6 of present timing cycle. Note that the waveforms CLEAR, READ, WRITE and some test waveforms (eg. TM18) whilst being set true at T6 of previous timing cycle are not in fact used until T5 of present timing cycle.

TITLE  
920 B CONTROL SEQUENCE. FUNCTIONS 0-9.  
SHEET 1 OF 2

A11

LATEST MI INCORPORATED; MI ~~2998~~

ISSUE No.	1	2						MATERIAL	FINISH	ELLIOTT BROTHERS (LONDON) LTD	
ALTN No.	1620	1769								SHEET 1 OF 2	
DATE	25.08.66	22.11.66								DRAWING No. 322 D 7880	
INITIALS	S.N.										
PARTS LIST No.											
DRAWN	S.NIBLETT.	CHECKED	C.S. 707	APPROVED	A.W. 22.11.66	DATE	30.11.66	TOLERANCES	DECIMAL	TITLE 920 B CONTROL SEQUENCE. FUNCTIONS 0-9.	
										UNLESS OTHERWISE STATED	